

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#9



Applicant: Peddle, Charles I.

Group Art Unit: 2786

Serial Number: 08/909,489

Examiner: Ramesh Patel

Filed: 08/12/97

Title: **MEMORY MODULE ASSEMBLY USING PARTIALLY DEFECTIVE CHIPS**


ASSISTANT COMMISSIONER FOR PATENTS AND TRADEMARKS
WASHINGTON, D. C. 20231

SIR:

COMMUNICATION TO THE DRAWING PROCESSING BRANCH

Enclosed herewith is the formal drawing in the above identified application. The enclosed drawing comprises 47 sheets with figures 1A through 7N.

Respectfully,

 10/1/99
John C. Albrecht (Reg. 18,373)

Address all correspondence to:
John C. Albrecht
1044 N. Second Ave.
St. Charles, IL 60174

RECEIVED
OCT 07 1999
Group 2700